

DATE: 1/27/00

FROM: BRADON (print name)

FORWARD TO:

A. Art Unit: 2781
B. Class: 710
C Subclass: _____

REASON(S):

A. You had Parent
B. See Title
C. See Abstract
D. See Claim(s): 2-3

<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)

FURTHER EXPLANATION IF NEEDED: Data bus circuit. Yours or lower level (class 326?)

DATE: 2/4/00

FROM: Shute (print name)

FORWARD TO:

A. Art Unit: 2787
B. Class: 713
C Subclass: 400

REASON(S):

A. You had Parent
B. See Title
C. See Abstract
D. See Claim(s): _____

<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)

FURTHER EXPLANATION IF NEEDED: matching different operating rates of DSP and CPU

DATE: _____

FROM: _____ (print name)

FORWARD TO CLASSIFIER

REASON(S):

A. You had Parent
B. See Title
C. See Abstract
D. See Claim(s): _____

<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)

FURTHER EXPLANATION IF NEEDED:

DISPOSITION BY 2700 CLASSIFICATION

DATE: _____

CLASSIFIER: _____

FORWARD TO:

A. Art Unit: _____
B. Class: _____
C Subclass: _____

REASON(S):

A. You had Parent
B. See Title
C. See Abstract
D. See Claim(s): _____

<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)
<input type="checkbox"/>	(check box)

FURTHER EXPLANATION IF NEEDED: